

**INFORMATION DISCLOSURE STATEMENT**

Applicant	:	Ayaki et al.
App. No	:	10/582,190
Filed	:	March 21, 2007
For	:	TAPE WITH BUILT-IN IC CHIP AND SHEET WITH BUILT-IN IC CHIP
Examiner	:	Brent T. O'Hern
Art Unit	:	1794

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing two (2) references to be considered by the Examiner.

This Information Disclosure Statement is being filed after the mailing date of a final action or after the mailing date of a Notice of Allowance. This Statement is accompanied by the fee set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

CERTIFICATION UNDER 37 C.F.R. § 1.97(e)(2)

I hereby certify that no item of information contained in this Statement was cited in a communication from a foreign Patent Office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

**Appl. No.** : 10/582,190  
**Filed** : March 21, 2007

**Docket No.** SHIGA3.010APC  
**Customer No.** 20,995

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: \_\_\_\_\_

7/17/09

By: \_\_\_\_\_

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